

Meidika Dwi Alfian. 2022. *Pengembangan Media Aplikasi Simulasi Rangkaian Listrik Seri Dan Rangkaian Listrik Pararel Pada Kelas VI Tema 3 SDN Jati*. Skripsi, Program Studi Pendidikan Guru Sekolah Dasar, Fakultas Ilmu Pendidikan, Universitas Trunojoyo Madura. Pembimbing : Mahmud S.IP., M.SI.

ABSTRAK

Tujuan penelitian dan pengembangan ini adalah untuk mengetahui kevalidan, keefektifan, dan kemenarikan media aplikasi simulasi rangkaian listrik seri dan rangkaian listrik pararel pada kelas VI tema 3 SDN Jati. Model penelitian dan pengembangan ini menggunakan model *Borg and Gall* yang dimodifikasi oleh sugiyono yaitu : (1) Identifikasi masalah, (2) pengumpulan data, (3) desain produk, (4) validasi desain, (5) revisi desain, (6) uji coba produk, (7) revisi produk, (8) uji coba pemakaian, (9) revisi produk, (10) produksi massal. Kevalidan media aplikasi simulasi rangkaian listrik seri dan rangkaian listrik pararel diperoleh dari hasil 92,8% (sangat valid). Keefektifan media aplikasi simulasi rangkaian listrik seri dan rangkaian listrik pararel diperoleh dari lembar observasi aktivitas guru 92% (sangat baik), lembar observasi aktivitas siswa 92,7% (sangat baik), dan ketuntasan klasikal hasil belajar 100% (sangat baik). Kemenarikan media aplikasi simulasi rangkaian listrik seri dan rangkaian listrik pararel diperoleh dari angket respon siswa sebesar 96,15% (sangat menarik). Hasil penelitian dan pengembangan media aplikasi simulasi rangkaian listrik seri dan rangkaian listrik pararel menunjukkan kriteria sangat valid, sangat efektif, dan sangat menarik sehingga dapat digunakan sebagai media pembelajaran di SDN Jati Kecamatan Soko Kabupaten Tuban.

Kata Kunci: *Media Aplikasi Simulasi, Rangkaian Listrik Seri dan Rangkaian Listrik Pararel*

Meidika Dwi Alfian. 2022. *Development of Application Media for Simulation of Series and Parallel Electric Circuits in Class VI Theme 3 SDN Jati*. Thesis, Elementary School Teacher Education Study Program, Faculty of Education, Trunojoyo University, Madura. Supervisor : Mahmud S.IP., M.SI.

ABSTRACT

The purpose of this research and development is to determine the validity, effectiveness, and attractiveness of the simulation application media for series electric circuits and parallel electric circuits in class VI theme 3 SDN Jati. This research and development model use the Borg and Gall model modified by Sugiyono, namely: (1) Problem identification, (2) data collection, (3) product design, (4) design validation, (5) design revision, (6) test product trial, (7) product revision, (8) usage trial, (9) product revision, (10) mass production. The validity of the simulation application media for series and parallel electrical circuits was obtained from the results of 92.8% (very valid). The effectiveness of the application media for the simulation of series electrical circuits and parallel electric circuits was obtained from the teacher activity observation sheet 92% (very good), student activity observation sheet 92.7% (very good), and 100% classical completeness learning outcomes (very good). The attractiveness of the application media for the simulation of series electrical circuits and parallel electric circuits was obtained from a student response questionnaire of 96.15% (very interesting). The results of the research and development of the simulation application media for series electrical circuits and parallel electric circuits show very valid, very effective, and very interesting criteria so that they can be used as learning media at SDN Jati, Soko District, Tuban Regency.

Keyword: *Simulation Application Media, Series Electrical Circuits and Parallel Electrical Circuits*